

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	"20060010312".pn.	US-PGPUB; USPAT	OR	OFF	2008/01/15 17:30
L3	2465	address adj space near3 (common shar\$3)	US-PGPUB; USPAT	OR	OFF	2008/01/15 17:34
L4	0	address adj space near3 (common shar\$3) with code\$1 near3 load\$3	US-PGPUB; USPAT	OR	OFF	2008/01/15 17:31
L5	5	address adj space near3 (common shar\$3) same common near2 code\$2	US-PGPUB; USPAT	OR	OFF	2008/01/15 17:32
L6	131	(processor cpu) with (instruction\$1 code\$1) with own\$2 with load\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/15 17:53
L7	0	address adj space near3 (common shar\$3) same L6	US-PGPUB; USPAT	OR	OFF	2008/01/15 17:34
L8	0	(processor cpu) with (instruction\$1 code\$1) with own\$2 with load\$3 and 712/206.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/15 17:53
L9	2	(processor cpu) with (instruction\$1 code\$1) with own\$2 with load\$3 and 713/2.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/15 17:53
S1	224	du.xa.	USPAT	OR	OFF	2007/05/29 09:58
S2	245	(thuan near2 (du su)).xa,xp.	USPAT	OR	OFF	2005/09/26 13:09
S4	218	(thuan near2 (du su)).xa.	USPAT	OR	OFF	2005/09/21 13:59
S5	193	(processors cpus) with codecs	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 09:58

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S6	8	(processors cpus) with codecs with (correspo\$5 indevidual\$2 separat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 10:00
S7	11	(processors cpus) with codecs with (correspo\$5 individual\$2 separat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 10:02
S8	2049	(processors cpus) with (own\$2 program\$1) with (correspo\$5 individual\$2 separat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 10:03
S9	78	(processors cpus) with (own\$2 program\$1) with (correspo\$5 individual\$2 separat\$3) with load\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 10:19
S10	84	(processors cpus) with (instruction\$1 code\$1) with (correspo\$5 individual\$2 separat\$3) with load\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 11:00
S11	118	(processor cpu) with (instruction\$1 code\$1) with own\$2 with load\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 12:02
S12	2	(processor cpu) with (instruction\$1 code\$1) with own\$2 with load\$3 same multiprocessor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 11:08

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S13	21	(processor cpu) near3 (instruction\$1 code\$1) near3 correspond\$3 with load\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 12:05
S14	2	(processors cpus) near3 correspond\$3 near3 (instruction\$1 code\$1) with load\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 12:08
S15	0	(processors cpus) near3 correspond\$3 near3 (instruction\$1 code\$1) with replac\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/29 12:08